

DCE MICROCOMPUTER
SYSTEMS
DESIGNER'S HANDBOOK

DAI THE
MICROCOMPUTER
ENGINEERING
COMPANY

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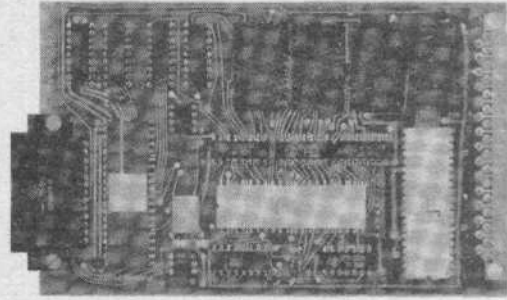
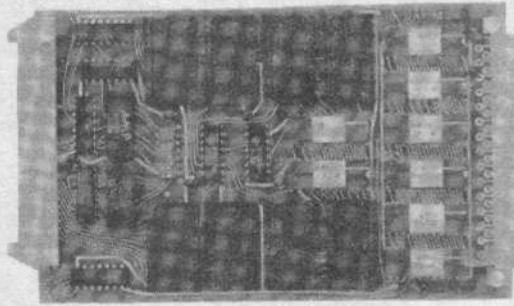
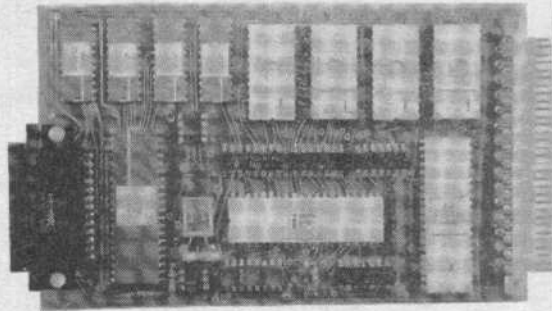
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Microcomputers...

COMPLETE

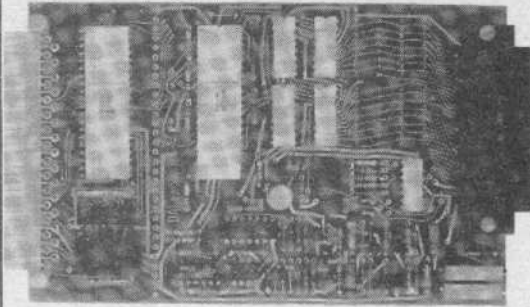
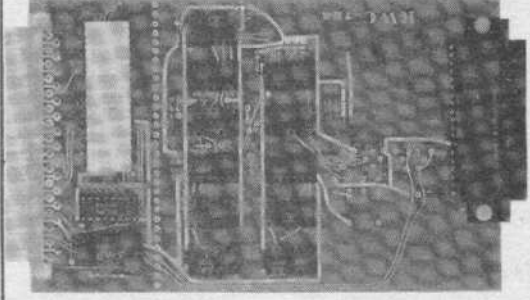
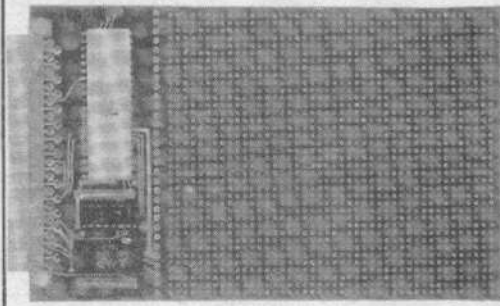
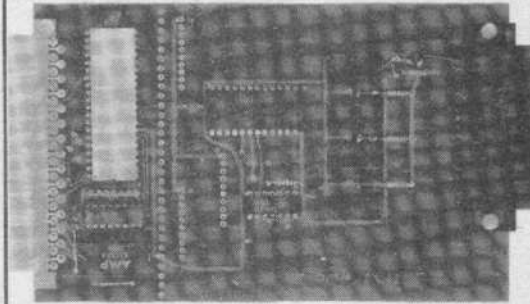
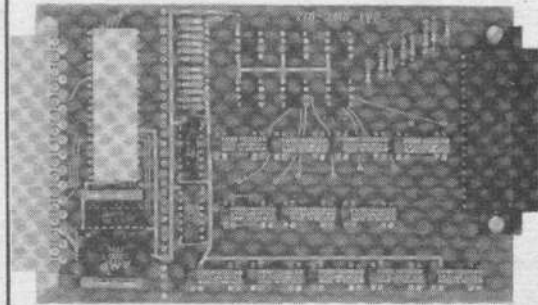
- microcomputer modules
- memory expansion modules
- real-world interface modules



COMPATIBLE

- bus compatible
- package compatible
- software compatible

D C C E - B U S



into the Real World !!

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